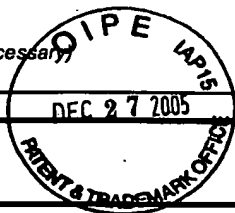


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Sheet 1 of 1



Applicant Number	10/725,898
Filing Date	December 1, 2003
First Named Inventor	San Wong
Art Unit	2133
Examiner Name	Not Yet Assigned
Attorney Docket Number	174/297
Confirmation Number	3367

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹		T ²
CB		"Jitter Fundamentals: Agilent N4900 Serial BERT Series Jitter Injection and Analysis Capabilities," Application Note, Agilent Technologies, November 2003, pp. 1-24.	
CB		"Understanding and Characterizing Timing Jitter," Tektronix, www.tektronix.com/jitter, 2003, pp. 1-24.	
CB		"Annex 48B: Jitter test methods," IEEE Std 802.3ae-2002, IEEE, 2002, pp. 505-514.	
CB		Wang, et al., "Equalization Techniques," Carleton University, presentation slides.	

Examiner
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